



PATENT  
Customer No. 22,852  
Attorney Docket No. 04329.2724

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Masatsugu KITADA et al. ) Group Art Unit: 2133  
Application No.: 10/050,160 ) Examiner: James C. Kerveros  
Filed: January 18, 2002 ) Confirmation No.: 7613  
For: SEMICONDUCTOR TESTING )  
APPARATUS AND METHOD FOR )  
OPTIMIZING A WAIT TIME UNTIL )  
STABILIZATION OF SEMICONDUCTOR )  
DEVICE OUTPUT SIGNAL )

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**REPLY TO OFFICE ACTION**

In reply to the Office Action mailed June 2, 2004, the period for response having been extended to November 2, 2004, by a request for extension of two months and fee payment filed concurrently herewith, please amend the above-identified application as follows:

**Amendments to the Specification** are included in this paper and begin on page 2.

**Amendments to the Claims** are reflected in the listing of claims in this paper and begin on page 3.

**Remarks/Arguments** follow the amendment sections of this paper.